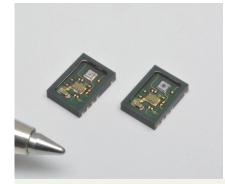


Photosensors with front-end IC



S15597-01CT S15658-01CT

Compact APD suitable for various light level detection

The S15597-01CT and S15658-01CT are compact optical devices that integrate a Si APD and a preamp. They have a builtin DC feedback circuit for reducing the effects of background light. They also provide excellent noise and frequency characteristics. We provide an evaluation kit for these products. Contact us for detailed information.

Features

- → High-speed response
- Two-level gain switch function (low gain: single output, high gain: differential output)
- Reduced background light effects
- Small waveform distortion when excessive light is incident

Applications

Distance measurement

Option

Driver circuit

C16188-03 (for S15597-01CT) C16189-03 (for S15658-01CT)

Structure

Parameter	Symbol	S15597-01CT	S15658-01CT	Unit
Detector	-	Si APD		
Photosensitive area size*1	Α	ф0.2	ф0.5	mm
Package	-	Glass epoxy		
Seal material	-	Silicone resin		

^{*1:} Photosensitive area in which a typical gain can be obtained

Absolute maximum ratings

Parameter	Symbol	Condition	Value	Unit
Supply voltage (for preamp)	Vcc max		4.5	V
Reverse voltage (for APD)	V_APD		0 to VBR	V
Reverse current (DC)	Ir max		0.2	mA
Forward current	IF max		10	mA
DCFB terminal voltage	-		Vcc + 0.7	V
Gain terminal voltage	-		Vcc + 0.7	V
Operating temperature	Topr	No dew condensation*2	-40 to +105	°C
Storage temperature	Tstg	No dew condensation*2	-40 to +125	°C
Soldering temperature*3	Tsol		260 °C (twice)	°C

^{*2:} When there is a temperature difference between a product and the surrounding area in high humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

^{*3:} Reflow soldering, IPC/JEDEC J-STD-020 MSL 4, see P.8

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

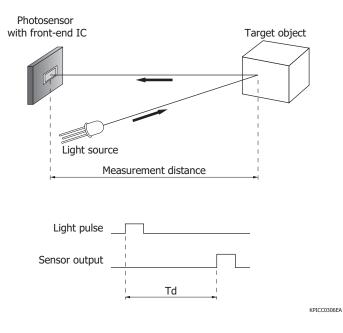
➡ Electrical and optical characteristics (Ta=25 °C)

Parameter	Symbol	Condition	S15597-01CT			S15658-01CT			Linit
		Condition	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
Supply voltage	Vcc1, Vcc2		3.135	3.3	3.465	3.135	3.3	3.465	V
Spectral response range	λ		400 to 1100			4	400 to 1100		
Peak sensitivity wavelength	λр	M=100	-	840	-	-	840	-	nm
Photosensitivity	S	λ=905 nm, M=100 Low gain	100	150	240	100	150	240	kV/W
		λ=905 nm, M=100 High gain	1500	3200	5500	1500	3200	5500	
Breakdown voltage	VBR	ID=100 μA	155	175	195	155	175	195	V
Temperature coefficient of breakdown voltage	ΔTVBR		0.8	1.0	1.2	0.8	1.0	1.2	V/°C
Current concumption	Ic	Low gain	15	25	34	15	25	34	- mA
Current consumption		High gain	17	26	37	17	26	37	
Low cutoff frequency	fcl	Low gain	-	0.01	-	-	0.01	-	MHz
		High gain	-	0.5	-	-	0.5	-	
High cutoff frequency	fch	Low gain	100	180	260	90	160	240	MHz
High cutoff frequency		High gain	90	160	230	80	150	220	IYIMZ
Output noise voltage	VON	f=20 MHz, M=100 High gain	-	2.0	-	-	2.0	-	mV rms
Output voltage level		Low gain	0.5	0.9	1.3	0.5	0.9	1.3	V
	-	High gain	0.5	1	1.5	0.5	1	1.5	V
Output offset voltage	Voffset	High gain	-	-	±100	-	-	±100	mV
Maximum output voltage	Vn-n may	Low gain	-	-0.5	-	-	-0.5	-	V
amplitude	Vp-p max	High gain	-	±0.7	-	-	±0.7	-	

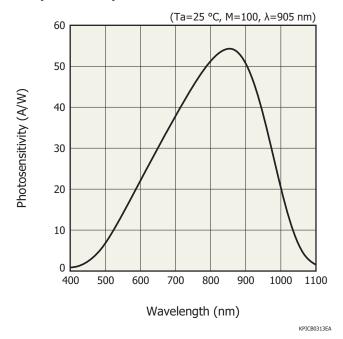
Distance measuring method

Distance L is calculated from the time difference Td between the light source's light emission timing and sensor output and the speed of light c.

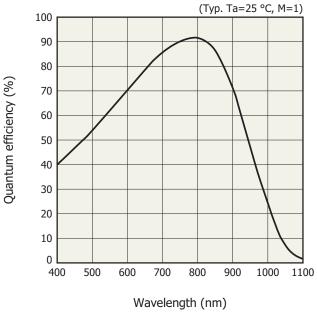
$$L = (1/2) \times c \times Td$$



Spectral response

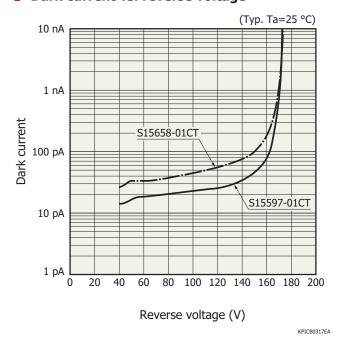


- Quantum efficiency vs. wavelength

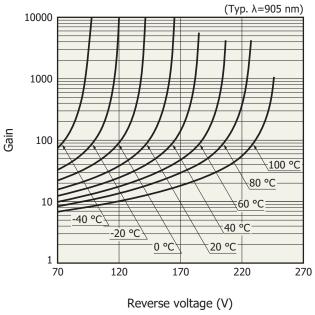


KAPDB0277EC

₽ Dark current vs. reverse voltage

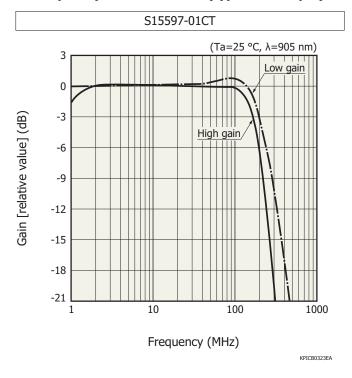


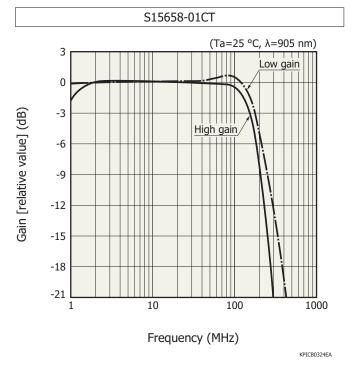
Gain vs. reverse voltage



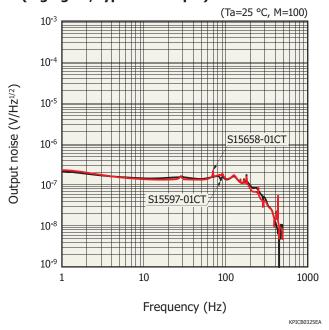
KPICB0314EA

Frequency characteristics (typical example)

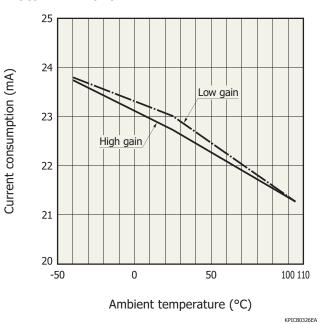




Output noise vs. frequency (high gain, typical example)



Current consumption vs. ambient temperature (typical example)



Truth table

■ Gain

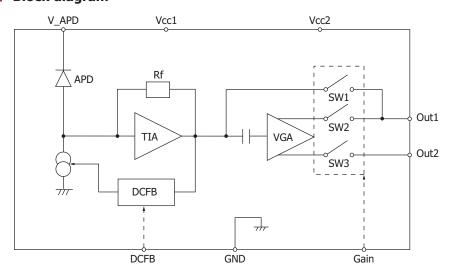
Setting	Gain
0	Low gain (× 1)
1	High gain (× 20)

■ DC feedback circuit

Setting	Background light elimination function		
0	OFF		
1	ON		

Note: The pull-up resistor of the digital input terminal is 10 k $\!\Omega.$

Block diagram



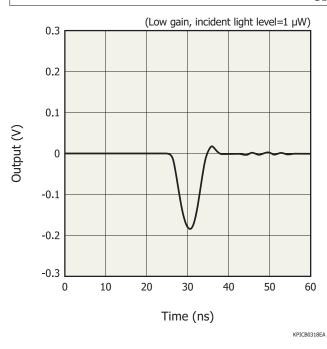
The DCFB (DC feedback) circuit detects the DC component of photocurrent, and reduces the effects of background light through the differential processor.

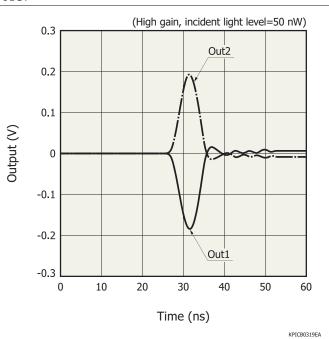
KPICC0285ED



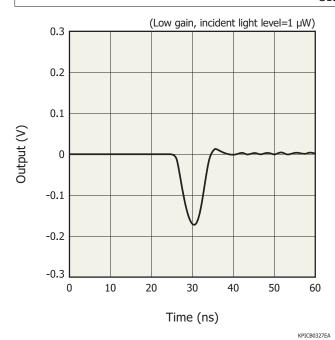
Output waveform examples (Ta=25 °C, input pulse width=5 ns)

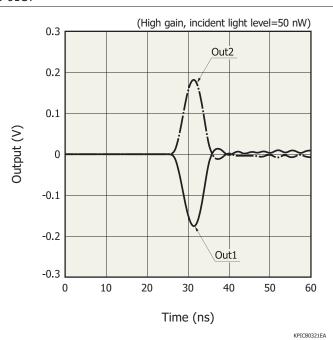




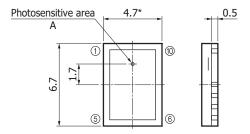


S15658-01CT

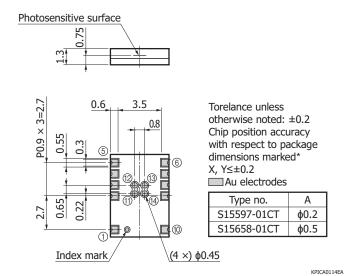




Dimensional outline (unit: mm)

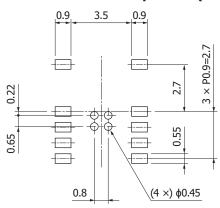


Pin no. Pin no. Function Function **DCFB** 8 Vcc1 1 9 2 Out1 **GND** 10 V_APD 3 Out2 4 **GND** 11 **GND** 5 **GND** 12 **GND** Gain 13 **GND** 6 7 14 Vcc2 **GND**



- Recommended land pattern (unit: mm)

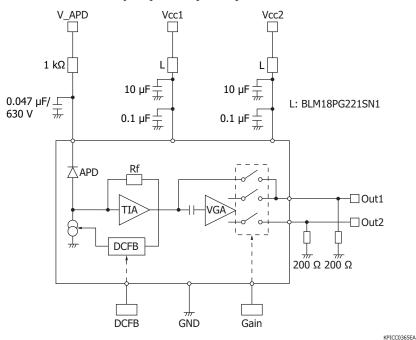
Pin connections



Torelance unless otherwise noted: ±0.1

KPICC0364EA

Connection example (50 Ω system)



When using the photosensor with front-end IC in a 50 Ω system, connect resistors with the same resistance (200 Ω in the above figure) to output loads Out1 and Out2. If resistors with the same resistance are not connected to the output loads, the waveform may be distorted or the output may oscillate.



Handling of temperature characteristics of APD gain

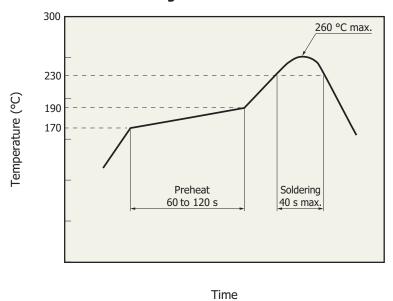
The gain of the APD built into the photosensor with front-end IC varies depending on the temperature. The following two methods are available for handling this issue in using the sensor over a wide temperature range.

① Temperature correction method, which controls the reverse voltage according to the temperature change
A thermistor or other temperature sensor is installed near the APD to measure the APD's temperature. The reverse voltage after
APD temperature correction is expressed by the following equation using temperature T of the APD.

 V_R (after temperature correction) = V_R (at 25 °C) + (T - 25) × ΔTV_{BR}

② Temperature control method, which keeps the APD temperature constant A TE-cooler or an equivalent device is used to maintain a constant APD temperature.

Recommended soldering conditions



 \cdot This product supports lead-free soldering. After unpacking, store it in an environment at a temperature of 30 °C or less and a humidity of 60% or less, and perform soldering within 72 hours.

KPICC0346EA

• The effect that the product receives during reflow soldering varies depending on the circuit board and reflow oven that are used. Before actual reflow soldering, check for any problems by testing out the reflow soldering methods in advance.

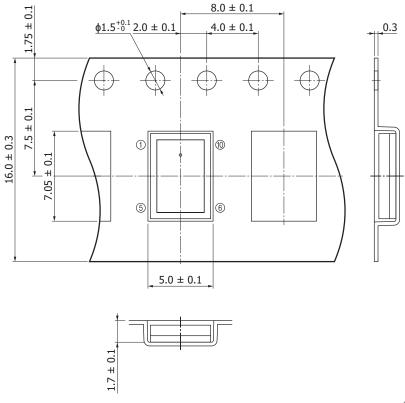


Reel packing specifications

■ Reel (conforms to JEITA ET-7200)

Outer diameter	Hub diameter	Tape width	Material	Electrostatic characteristics
φ254 mm	ф100 mm	16 mm	PS	Conductive

■ Embossed tape (unit: mm, material: PS, conductive)



KPICC0366EA

- Packing quantity 1000 pcs/reel
- Packing state

 Reel and desiccant in moisture-proof packing (vacuum-sealed)

- Related information

www.hamamatsu.com/sp/ssd/doc_en.html

- Precautions
- · Disclaimer
- $\cdot \ \text{Surface mount type products} \\$

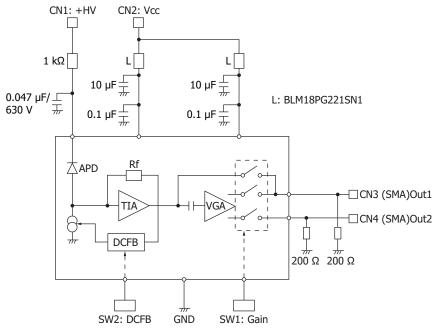
Evaluation kits for photosensor with front-end IC C16188-03, C16189-03

Evaluation kits [$48 \times 50 \ (H \times V) \ mm$] for photosensors with front-end IC are available [C16188-03 (with S15597-01CT), C16189-03 (with S15658-01CT)]. Contact us for detailed information.

Accessories

- · IC power cable
- · APD power cable

Equivalent circuit



KPICC0308EA

Information described in this material is current as of December 2021.

Product specifications are subject to change without prior notice due to improvements or other reasons. This document has been carefully prepared and the information contained is believed to be accurate. In rare cases, however, there may be inaccuracies such as text errors. Before using these products, always contact us for the delivery specification sheet to check the latest specifications.

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