

# **Photo IC diode**



S11153-01MT

# Wide operating temperature: -40 to +105 °C

The S11153-01MT photo IC has a spectral response close to human eye sensitivity. Two active areas are made on a single chip. Almost only the visible range can be measured by finding the difference between the two output signals in the internal current amplifier circuit. Compared to the previous type, the S11153-01MT has a wide operating temperature range (-40 to +105 °C).

#### Features

- Wide operating temperature: -40 to +105 °C
- Spectral response close to human eye sensitivity
- Lower output-current variation compared with phototransistors
- Excellent linearity
- Low output deviation by different color temperature light source
- Suitable for lead-free reflow (RoHS compliance)

### Applications

- → Automotive illuminance sensor
- Energy-saving sensor for TVs, etc.
- **▶** Various types of light level measurement

#### **♣** Absolute maximum ratings (Ta=25 °C)

Parameter	Symbol	Condition	Value	Unit
Reverse voltage	VR		-0.5 to +12	V
Photocurrent	IL		5	mA
Forward current	IF		5	mA
Power dissipation*1	Р		300	mW
Operating temperature	Topr	No dew condensation*2	-40 to +105	°C
Storage temperature	Tstg	No dew condensation*2	-40 to +125	°C
Reflow soldering conditions*3	Tsol		Peak temperature 250 °C max., two times	-

<sup>\*1:</sup> Power dissipation decreases at a rate of 3.0 mW/°C above Ta=25 °C.

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

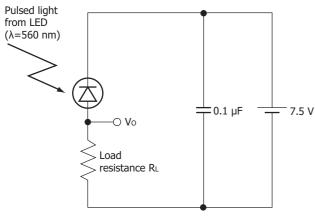
<sup>\*2:</sup> When there is a temperature difference between a product and the surrounding area in high humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

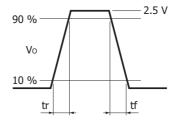
<sup>\*3:</sup> JEDEC level 4

# **➡** Electrical and optical characteristics (Ta=25 °C)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Spectral response range	λ		-	300 to 820	-	nm
Peak sensitivity wavelength	λр		-	560	-	nm
Dark current	ID	VR=5 V	-	1.0	50	nA
Photocurrent	IL	VR=5 V, 2856 K, 100 lx	325	-	495	μA
Rise time*4	tr	10 to 90%, VR=7.5 V RL=10 kΩ, λ=560 nm	-	6.0	-	ms
Fall time*4	tf	90 to 10%, VR=7.5 V RL=10 kΩ, λ=560 nm	-	2.5	-	ms

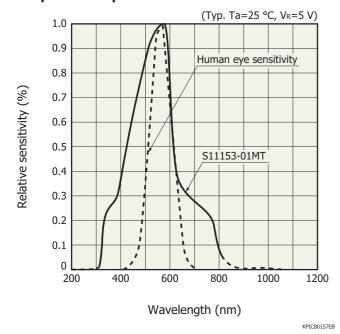
# \*4: Rise/fall time measurement method



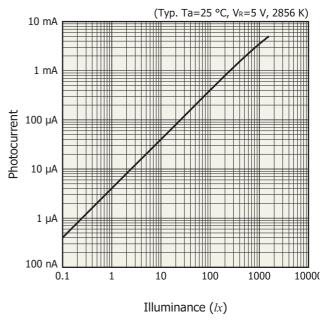


KPICC0041EA

# - Spectral response

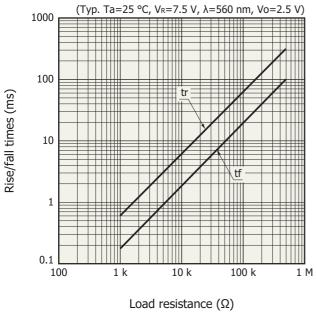


#### Photocurrent vs. illuminance



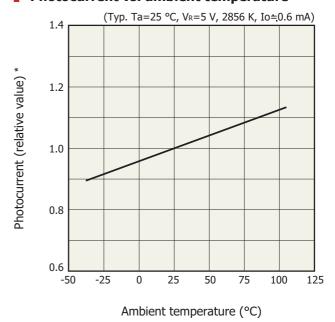
KPICB0158EB

# - Rise/fall times vs. load resistance



KPICB0115EA

# - Photocurrent vs. ambient temperature

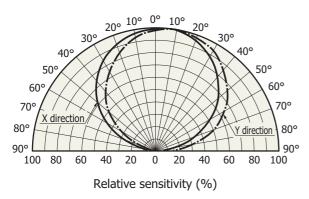


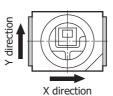
\* Normalized photocurrent 1 at Ta=25 °C

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### Directivity

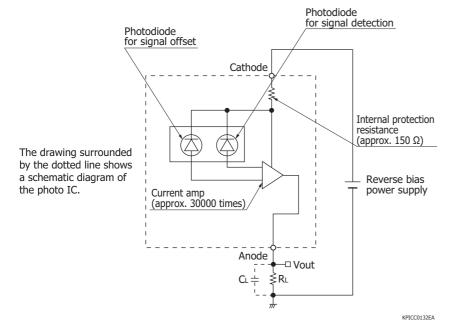
(Typ. Ta=25 °C, tungsten lamp)





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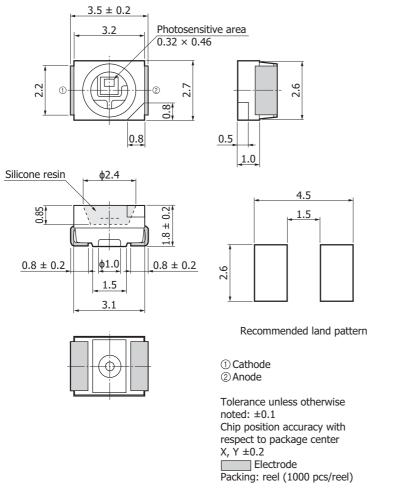
#### **Block diagram**



The photo IC diode must be reverse-biased so that a positive potential is applied to the cathode. To eliminate high-frequency components, we recommend placing a load capacitance C<sub>L</sub> in parallel with load resistance R<sub>L</sub> as a low-pass filter.

Cut-off frequency fc 
$$\approx \frac{1}{2\pi \text{ CL RL}}$$

# **Dimensional outline (unit: mm)**



KPICA0087EB

#### Operating voltage, output characteristics

Figure 2 shows the photocurrent vs. reverse voltage characteristics (light source: LED) for the measurement circuit example in Figure 1. The output curves are shown for illuminance levels. The output curves rise from a reverse voltage (rising voltage) of approximately  $0.7 \text{ V } (\pm 10\%)$ .

To protect the photo IC diode from excessive current, a 150  $\Omega$  ( $\pm$ 20%) protection resistor is inserted in the circuit. Reverse voltage V<sub>R</sub> when the photo IC diode is saturated is the sum of Vbe(ON) and the voltage drop across the protection resistor Rin [Equation (1)].

$$V_R = Vbe(ON) + I_L \times Rin \dots (1)$$

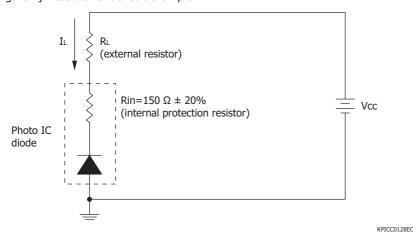
The photodiode's reverse voltage  $(V_R)$  is expressed by Equation (2) according to the voltage drop across the external resistor. This is indicated as load lines in Figure 2.

$$V_R = V_{CC} - I_L \times R_L \dots (2)$$

In Figure 2, the intersections between the output curves and the load lines are the saturation points. From these points, the maximum detectable light level can be specified. Since the maximum light level is determined by the supply voltage (Vcc) and load resistance ( $R_L$ ), adjust them according to the operating conditions.

Note: The temperature characteristics of Vbe(ON) is approximately -2 mV/°C, and that of the protection resistor is approximately 0.1%°C.

[Figure 1] Measurement circuit example

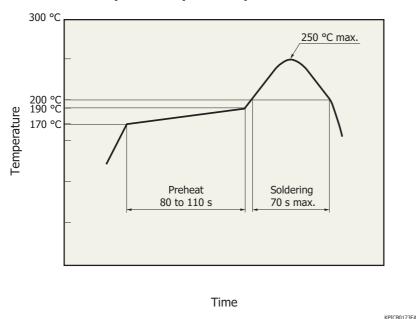


[Figure 2] Photocurrent vs. reverse voltage

(Typ. Ta=25 °C) 1100 lx Internal protection resistance Rin: Approx. 150 Ω ₹1000 lx Saturation region Photocurrent (mA) Approx. 850 b 800 lx 3 600 lxLoad line 2 Vcc=5 V, RL=1 kΩ Saturation region 450 lx Approx. 470 lx Load line Vcc=3 V, RL=1 k $\Omega$ 220 lx 1 0 0 Reverse voltage (V) Rising voltage



#### Measured example of temperature profile with our hot-air reflow oven for product testing



· This product supports lead-free soldering. After unpacking, store it in an environment at a temperature of 30 °C or less and a humidity of 60% or less, and perform soldering within 72 hours.

· The effect that the product receives during reflow soldering varies depending on the circuit board and reflow oven that are used. Before actual reflow soldering, check for any problems by testing out the reflow soldering methods in advance.

#### Related information

www.hamamatsu.com/sp/ssd/doc\_en.html

- Precautions
  - Disclaimer
  - · Surface mount type products

Information described in this material is current as of June 2017.

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